

IN THE CLAIMS:

Claim 1 (currently amended): A laminated ceramic substrate formed by laminating ceramic layers each having a circuit element pattern formed on a surface thereof, the laminated ceramic substrate ~~comprising~~ further including a side electrode comprising in ~~which~~ a side edge electrode layer formed on a side edge portion of the each ceramic layer which overlaps with and connects to a side edge electrode layer formed on a side edge portion of another ceramic layer directly above and/or directly below ~~the former~~ an adjacent ceramic layer, ~~the each~~ side edge electrode layer ~~comprising~~ positioned within a through hole in the ceramic substrate, the through hole bounded by a side surface of the laminated ceramic substrate, a parallel wall unexposed and approximately parallel to [[a]] the side surface of the laminated ceramic substrate and [[a]] two perpendicular wall walls approximately perpendicular to the side surface of the laminated ceramic substrate, a length La of the parallel wall and a depth Lb of the parallel wall from the side surface of the laminated ceramic substrate having a relationship of $L_a > L_b$.

Claim 2 (currently amended): A laminated ceramic substrate according to claim 1, wherein ~~the parallel wall and each perpendicular wall are~~ is connected to the parallel wall by a corner portion with ~~an R-shape~~ a circular-arc shape of a radius R in which R is greater than 0.02 mm.

Claim 3-20 (canceled):

Claim 21 (new): A laminated ceramic substrate formed by laminating ceramic layers each having a circuit element pattern formed on a surface thereof, the laminated ceramic substrate further including opposite side electrodes each comprising a side edge electrode layer formed on a side edge portion of each ceramic layer within overlaps with and connects to a side edge electrode layer formed on a side edge portion of another ceramic layer directly above and/or directly below an adjacent ceramic layer, each side edge electrode layer positioned within a through hole in the ceramic substrate, the through hole bounded by a side surface of the laminated ceramic substrate, a wall approximately parallel to the side surface of the laminated ceramic substrate and two perpendicular walls approximately perpendicular to the side surface of the laminated ceramic substrate, a length L_a of the parallel wall and a depth L_b of the parallel wall from the side surface of the laminated ceramic substrate having a relationship of $L_a > L_b$.

Claim 22 (new): A laminated ceramic substrate according to claim 21, wherein each perpendicular wall is connected to the parallel wall by a corner portion with a circular-arc shape of a radius R in which R is greater than 0.02 mm.

Claim 23 (New): A laminated ceramic substrate according to claim 21, wherein the depth amount of opposite side edge electrode layers on at least one ceramic layer differs

from the depth amount of opposite side edge electrode layers on the other ceramic layers.

Claim 24 (New): A laminated ceramic substrate according to claim 22, wherein the depth amount of opposite side edge electrode layers on at least one ceramic layer differs from the depth amount of opposite side edge electrode layers on the other ceramic layers.